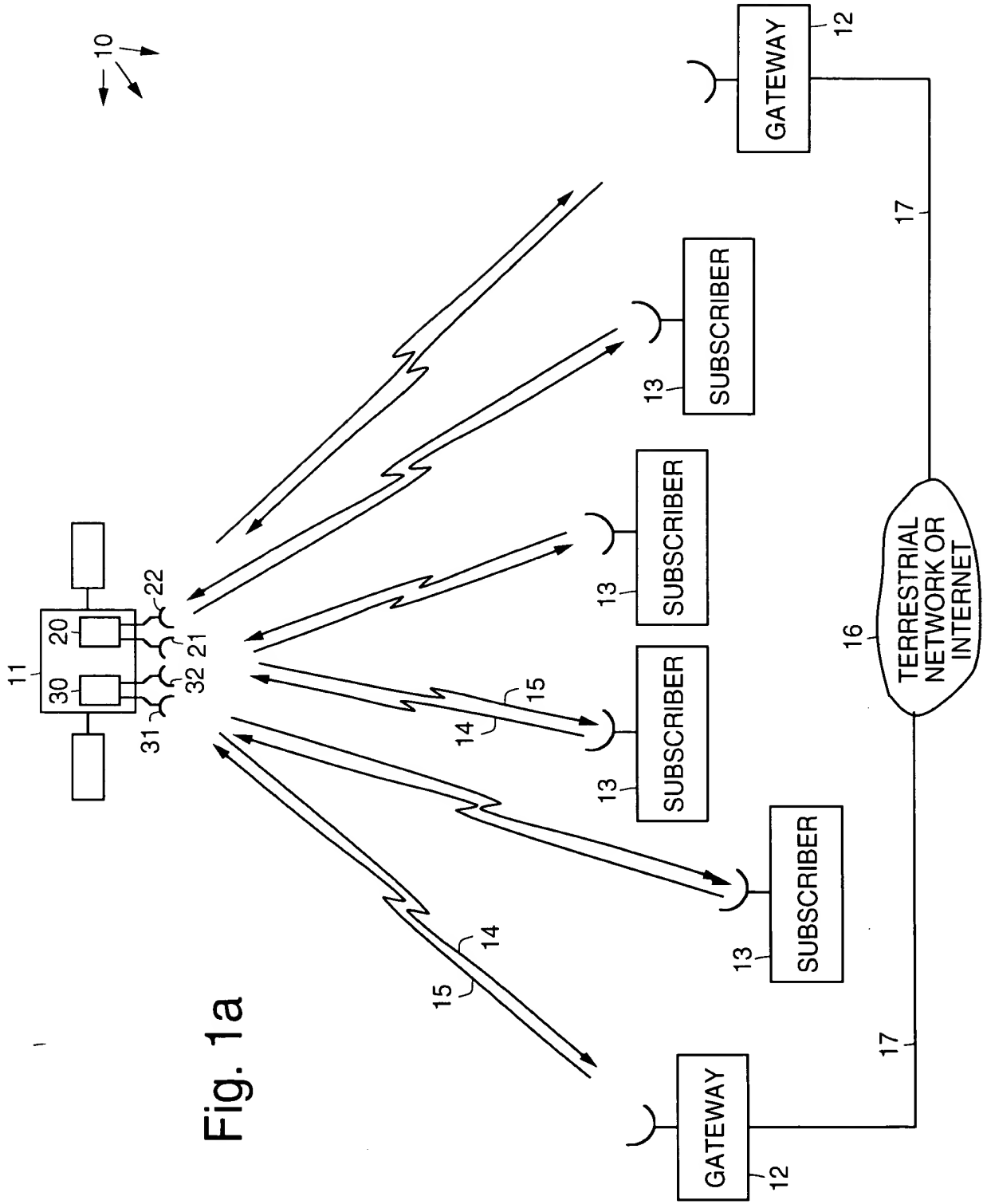


Fig. 1



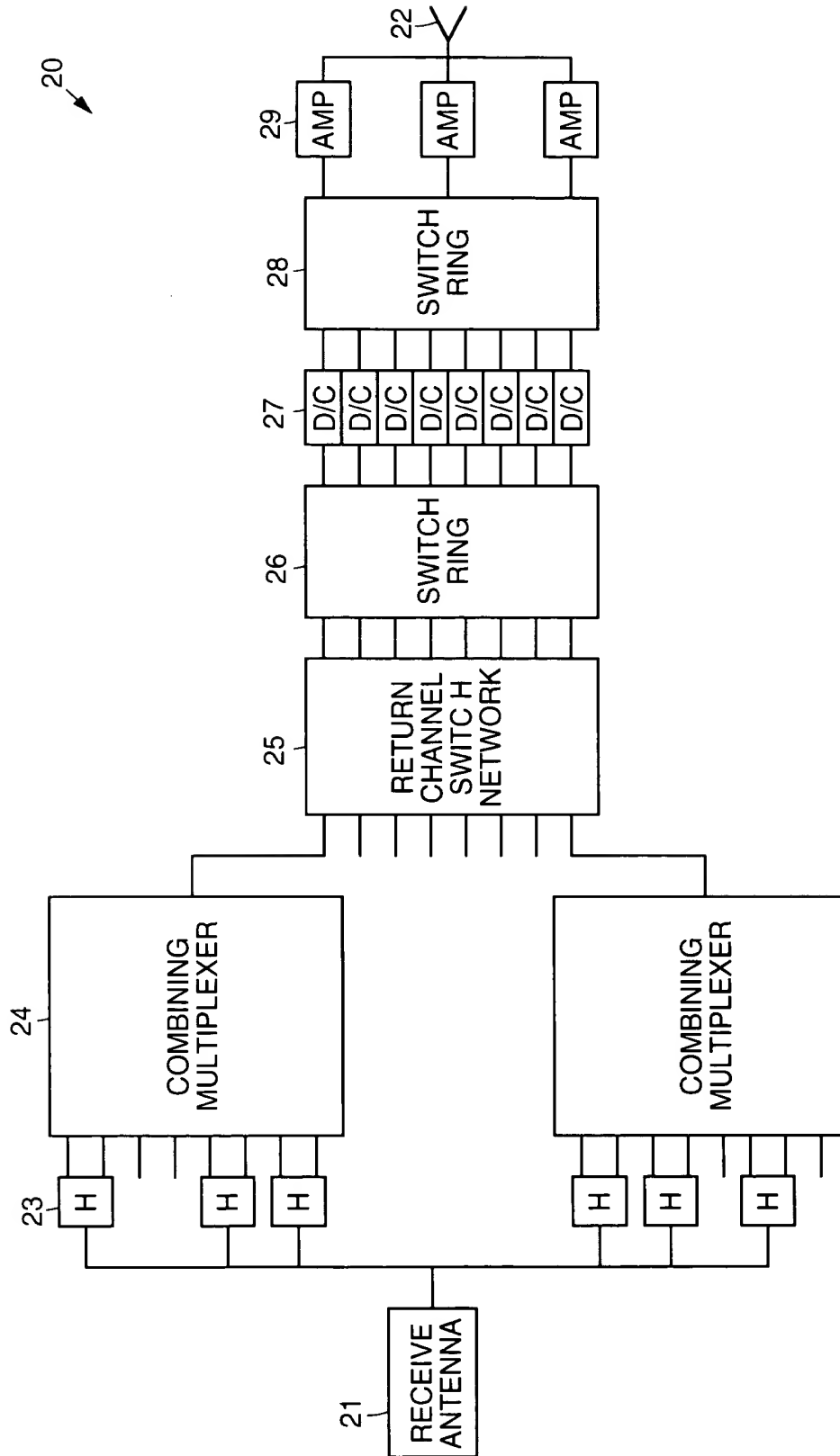


Fig. 2

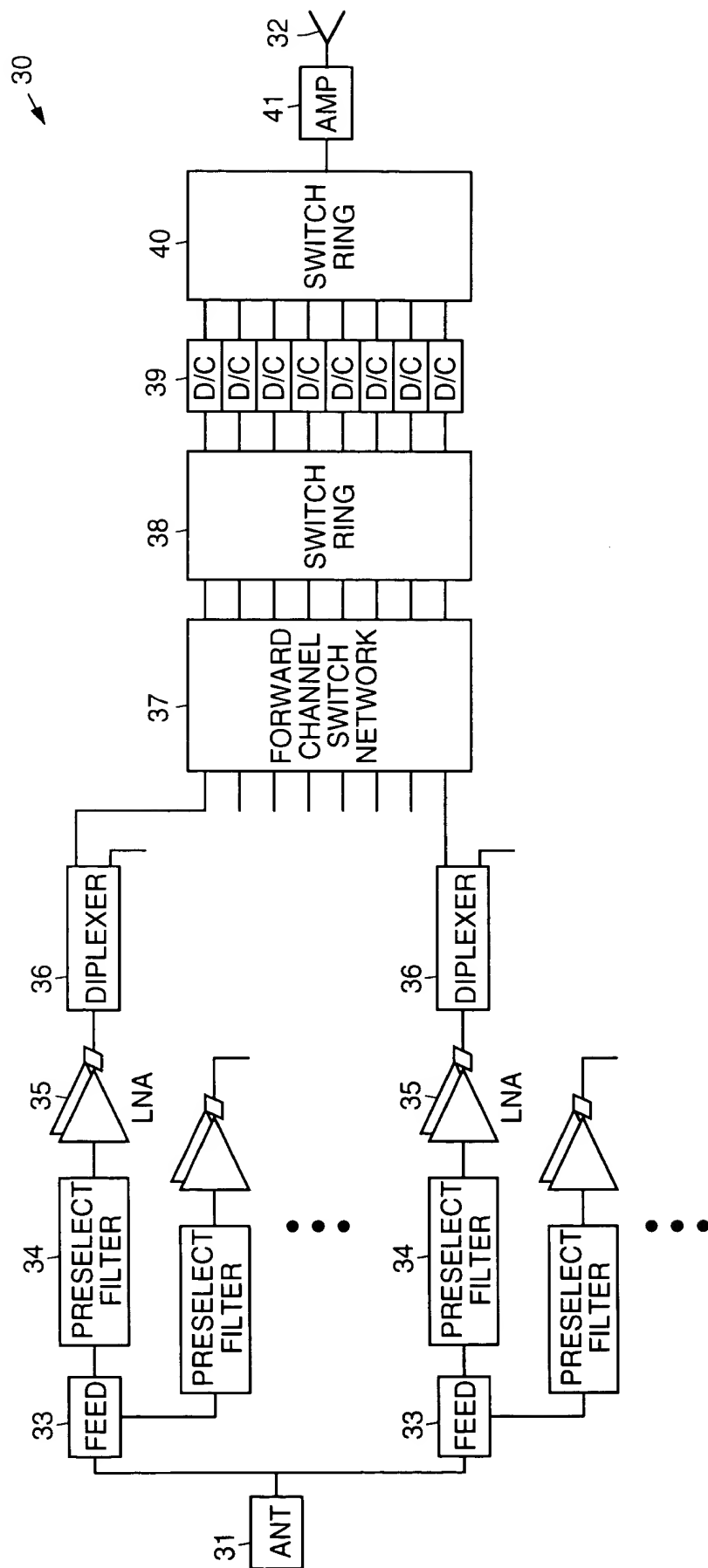
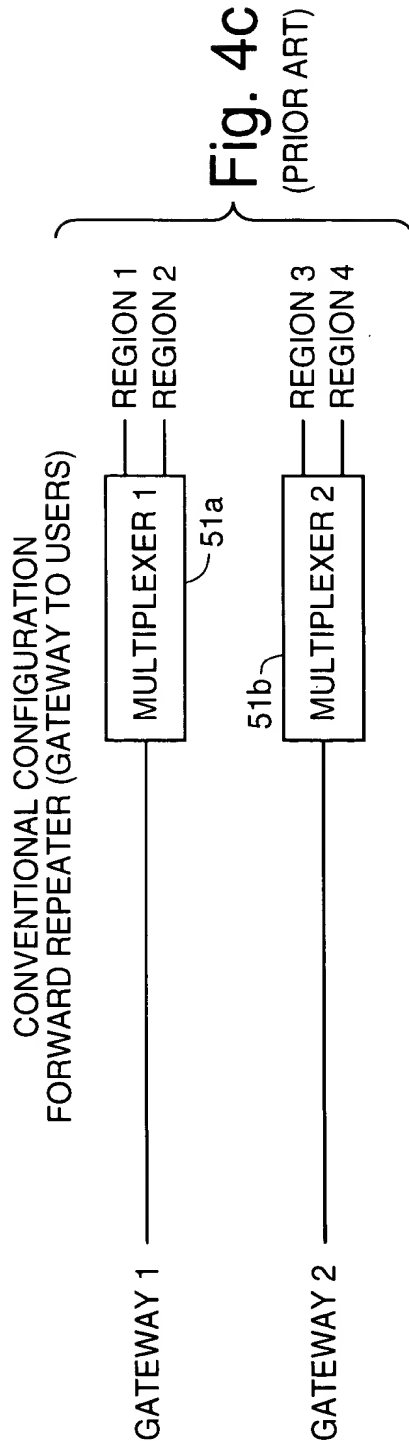
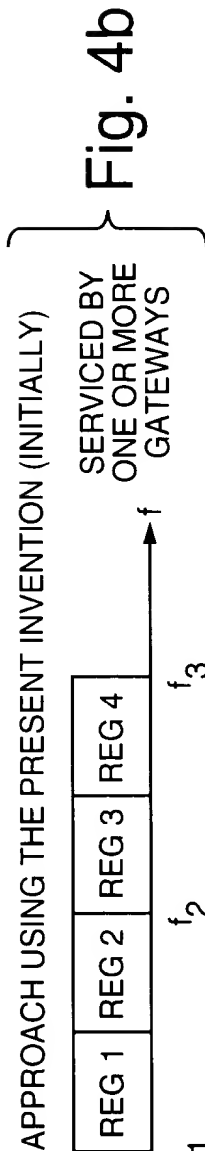
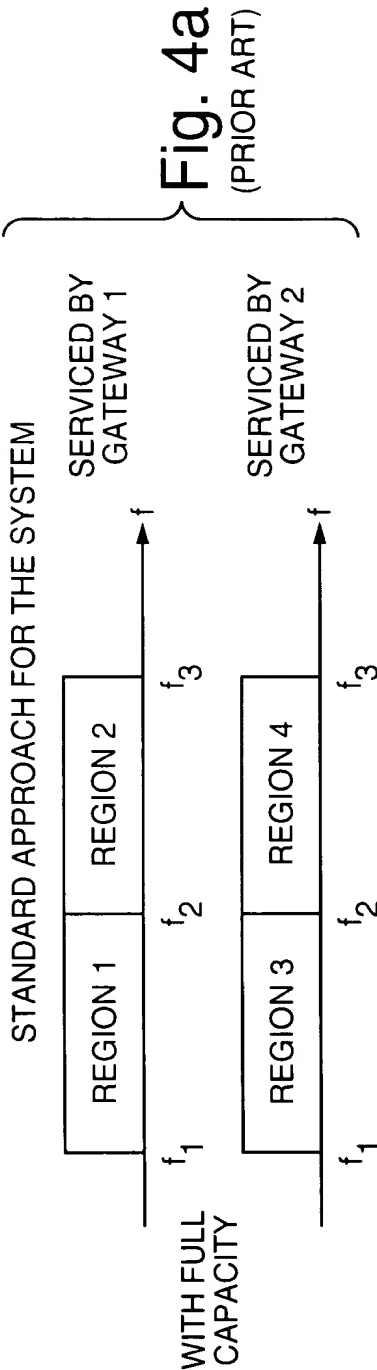
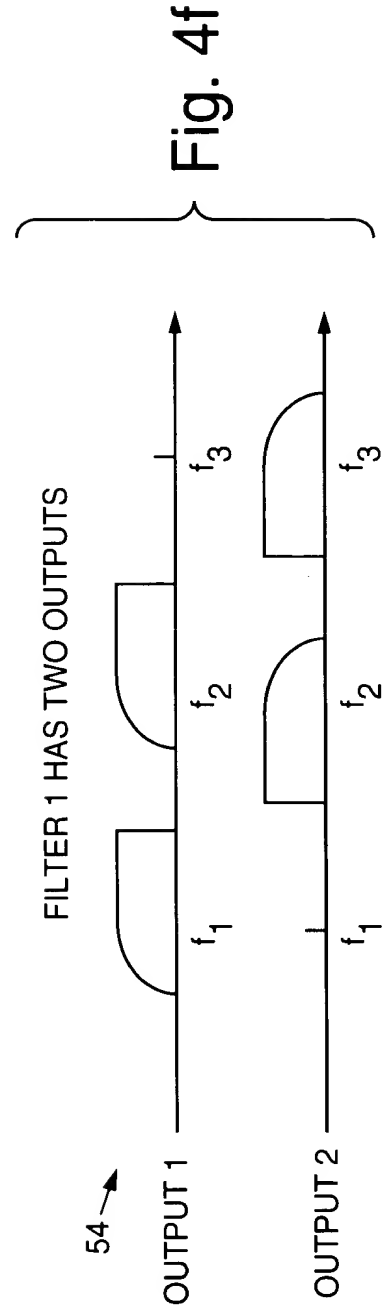
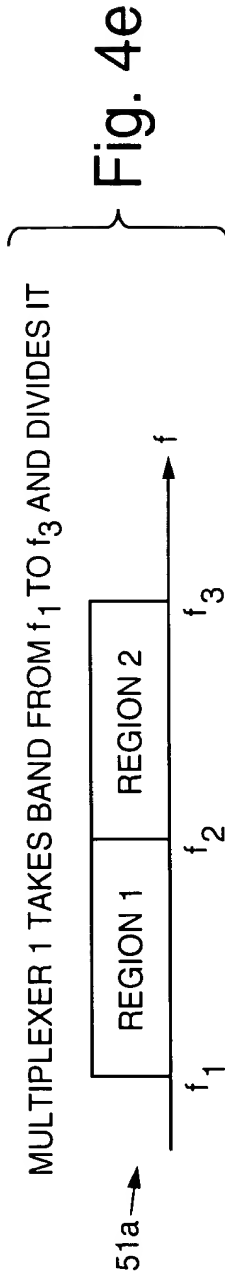
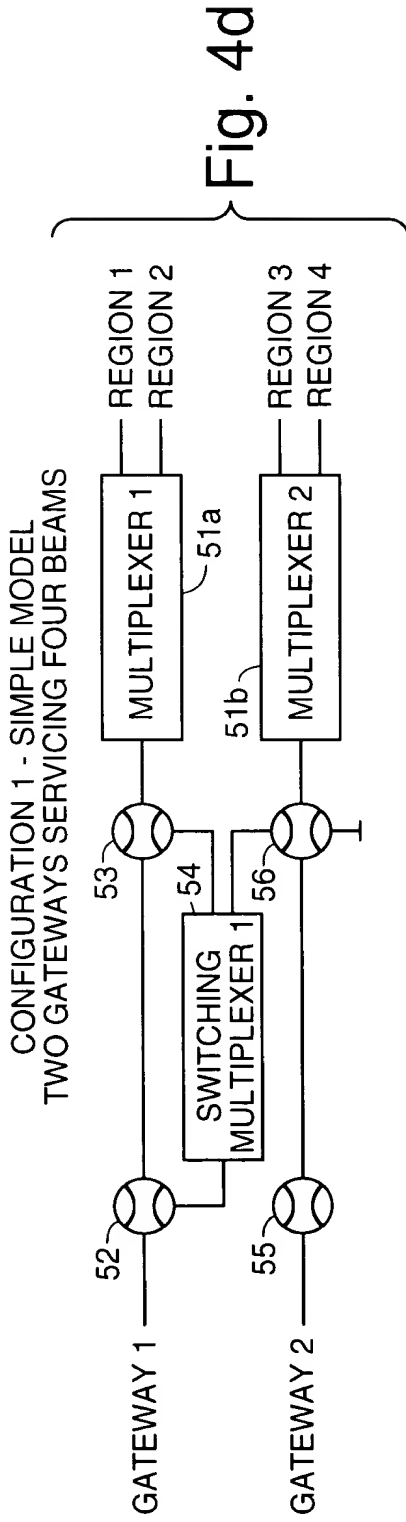


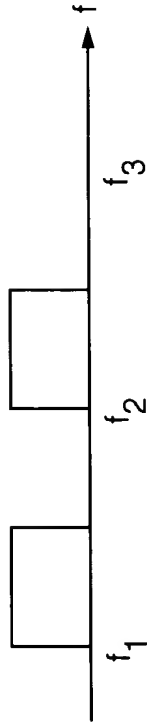
Fig. 3





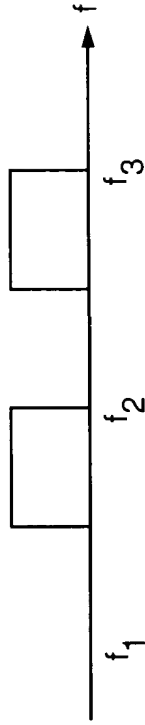
COMBINING OUTPUT 1 WITH MULTIPLEXER 1 GIVES

Fig. 4g



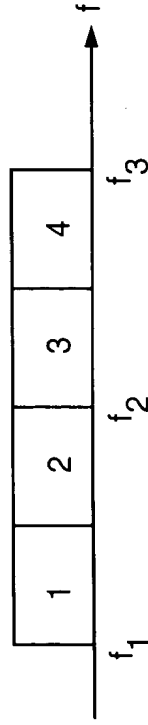
COMBINING OUTPUT 2 WITH MULTIPLEXER 1 GIVES

Fig. 4h



THE FILTER OUTPUT COMBINED WITH THE MULTIPLEXER
OUTPUT MEETS CHANNELIZATION REQUIREMENTS AND THE
SWITCH ETWORKS ROUTE THE BANDWIDTH APPROPRIATELY

Fig. 4i



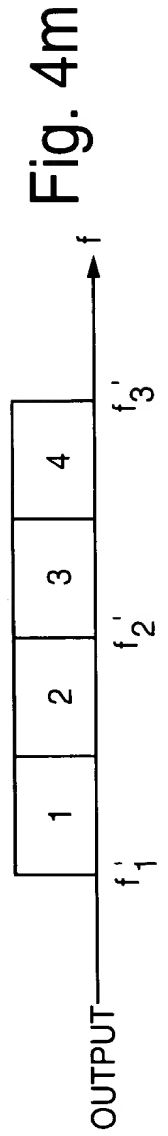
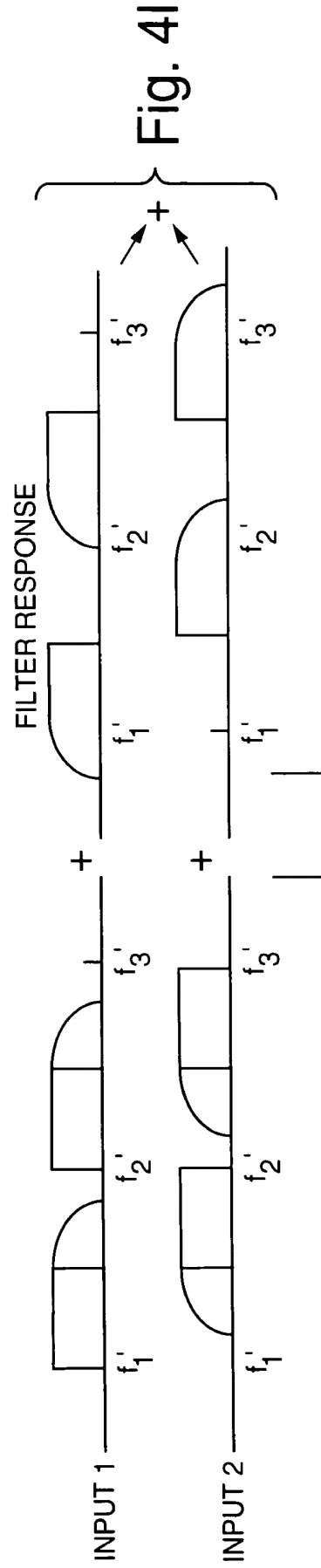
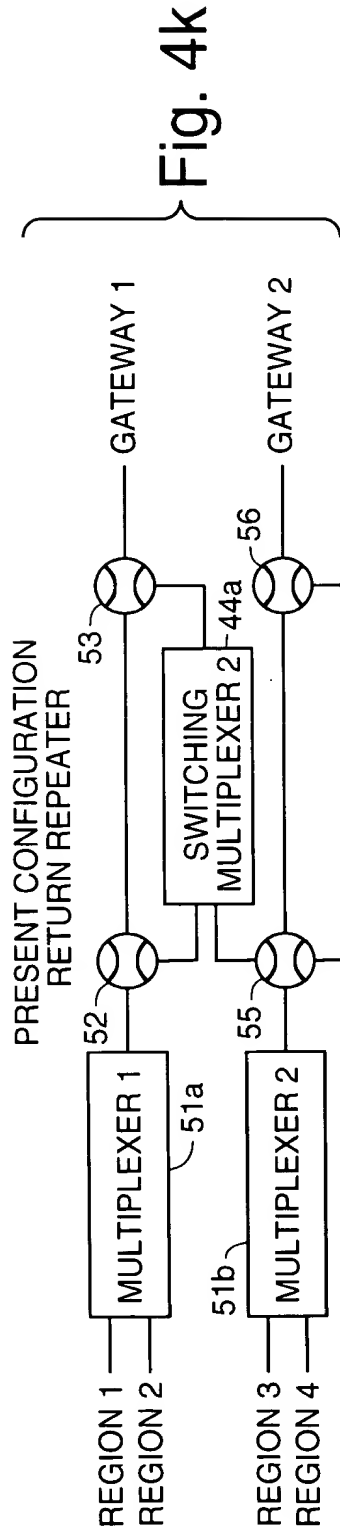
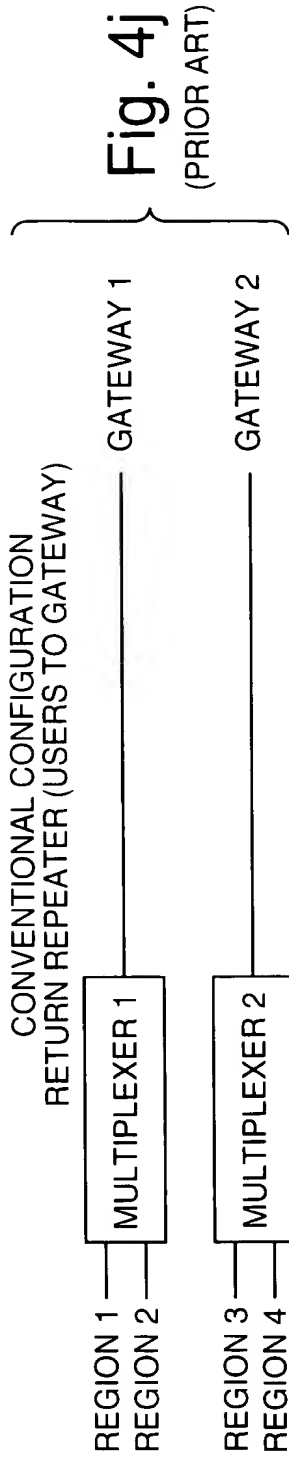


FIG. 5

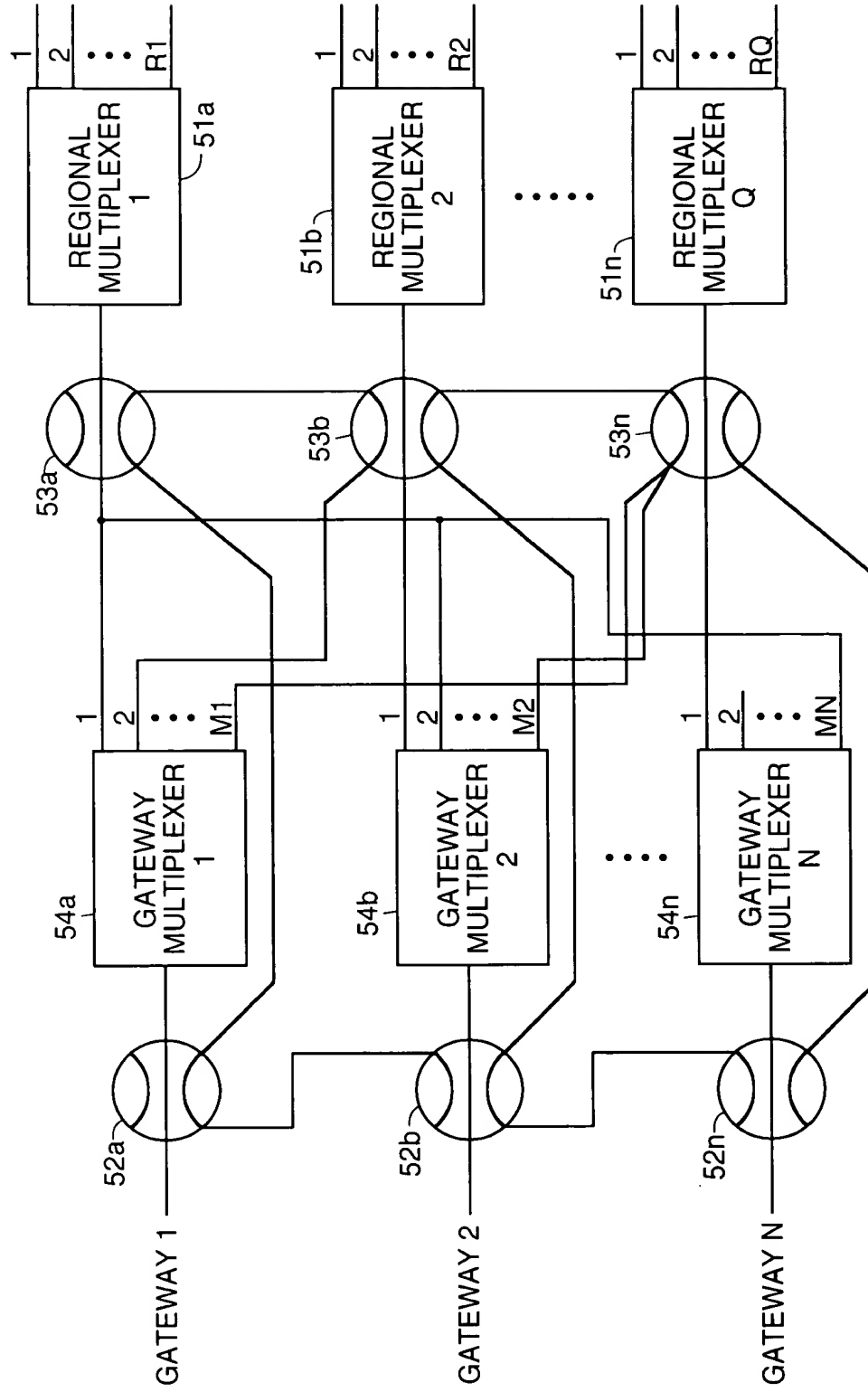


Fig. 5

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